Approach to Wide-Frequency Battery Impedance Measurements in Commercial Applications

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Abstract—**Electrochemical Impedance Spectroscopy (EIS) is a well-established diagnostic technique for the characterization and monitoring of electrochemical devices, such as batteries. As different electrochemical processes have very different dynamics, a complete characterization of batteries requires measuring their impedance over a very wide frequency range, from less than 1 mHz to more than 1 kHz. However, significant measurement challenges affect the extreme frequencies and may limit the practically achievable frequency range, particularly in case of *in situ* EIS. This paper analyzes the main measurement challenges that appear at low and high frequencies, respectively, and proposes possible strategies, suitable to be used in commercial applications. A compensation for the effect of the state-of-charge variation is proposed to improve the accuracy of EIS at low frequencies, while a strategy to measure high-frequency perturbations with a lower sampling frequency is presented to achieve high-frequency EIS with affordable instrumentation. Experimental results obtained on a lithium iron phosphate cell are reported, used to validate the proposed methods.**

Index Terms—**Batteries, Electrochemical devices, Electrochemical impedance spectroscopy, Measurement, Condition monitoring, State estimation**

I. Introduction

The state and dynamic behavior of batteries, as well as other electrochemical power sources, is characterized by a complex interplay of several physical and chemical processes, which evolve on very different time scales, from microseconds to years [1]. An effective method to analyze those processes and distinguish between their different contributions to the electrical output of the battery is the Electrochemical Impedance Spectroscopy (EIS): An AC perturbation is created in the battery current (or voltage), at a certain frequency, and the voltage (or current) response to that perturbation is measured, from which the impedance at that frequency is determined. Measuring the impedance at different frequencies allows characterizing different processes, from the fast electric response (ohmic phenomena) to the slower charge transfer and mass transport processes [1]. A generic illustration of a typical battery impedance spectrum is reported in Fig. 1.
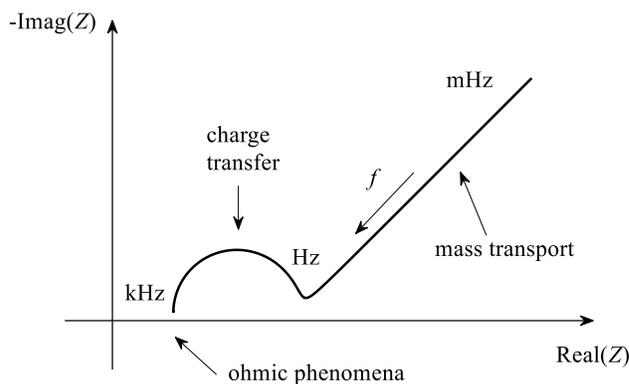
Fig. 1. Illustration of a generic battery impedance spectrum.

EIS has been widely investigated and used in the last couple of decades, and it has informed the development of battery models, particularly in terms of equivalent electrical circuits [2]–[6]. Battery impedance spectra reported in the literature cover frequencies down to a few millihertz (in some cases even down to a few microhertz [2]), and up to a few kilohertz or tens of kilohertz. Such extreme frequency ranges give rise to different types of measurement challenges. At low frequencies, transients in the battery state represent the main issue. By definition, the concept of impedance relies on the assumption of steady-state AC conditions; this means that, ideally, the battery should not undergo any change in its internal state, including in particular its state of charge, during the whole time required to measure the impedance, which can be up to several minutes or even hours at the low frequencies mentioned above. This may be feasible under accurately controlled laboratory conditions, and with no DC current generated/absorbed by the battery, but it becomes unfeasible in case of *in situ* measurements. On the other hand, high-frequency impedance measurements normally require instruments with a high sampling rate to acquire the high-frequency perturbations in the battery voltage and current. While those instruments are commonly available in a laboratory environment, they may not be affordable in commercial applications due to cost and complexity con-

straints.

To date, most of the EIS measurements reported in the literature have been carried out in the laboratory, mainly with the aim to improve battery models, characterize material properties, or investigate degradation phenomena. The practical relevance of the challenges arising from *in situ* EIS has therefore been limited. However, recent works have proposed solutions to effectively achieve EIS in commercial applications, by using power converters to create the required AC perturbations, in place of the dedicated instrumentation commonly used in the laboratory [7]–[16]. In most applications, batteries are connected to a switch-mode DC-DC converter; the AC perturbation in the battery current (or voltage) can therefore be conveniently created by modulating the duty cycle of the converter switching function. As most DC-DC converters nowadays reach switching frequencies in the range of tens or hundreds of kilohertz (particularly at low power), AC perturbations can cover the whole frequency range required for a complete EIS characterization, or most of it, on the proviso that the converter design allows small high-frequency components on the battery side (i.e. it does not filter them out completely). Therefore, the method itself does not introduce any significant limitation to the EIS frequency range, but the challenges mentioned above can practically limit it. Indeed, most of the feasibility studies of converter-based EIS reported in the literature to date have measured the impedance only in a medium-high frequency range (above 1 Hz in most cases), on small-scale systems composed of one or a few cells.

This paper analyzes the main challenges that are likely to hinder low-frequency and high-frequency impedance measurements, respectively, and proposes strategies to effectively address some of those challenges in commercial applications and to assess the validity of EIS results. The assumptions underlying impedance measurements are firstly reviewed in Sec. II, together with some considerations on their practical impact on EIS. The challenges and proposed strategies for low-frequency and high-frequency EIS are then presented in Secs. IV and V, respectively, after describing the experimental setup used for the empirical validation in Sec. III.

II. Battery Model and Impedance Measurement

Fig. 2a shows a generic equivalent circuit model (ECM) describing an electrochemical cell. It consists of a source of electromotive force (EMF) in series with an internal impedance. The value of the EMF can be measured under open circuit conditions as the open circuit voltage, V_{OCV}, whereas the impedance models the internal voltage drop appearing when current is drawn from the cell, caused by the various phenomena mentioned above.

Both EMF and internal impedance are functions of the battery state of charge (SoC); additionally, the impedance is a function of the current drawn from the cell, as the I-V characteristic of the battery is nonlinear. These two aspects pose significant challenges for EIS. It is therefore crucial to define the conditions that must be satisfied for a valid impedance measurement.
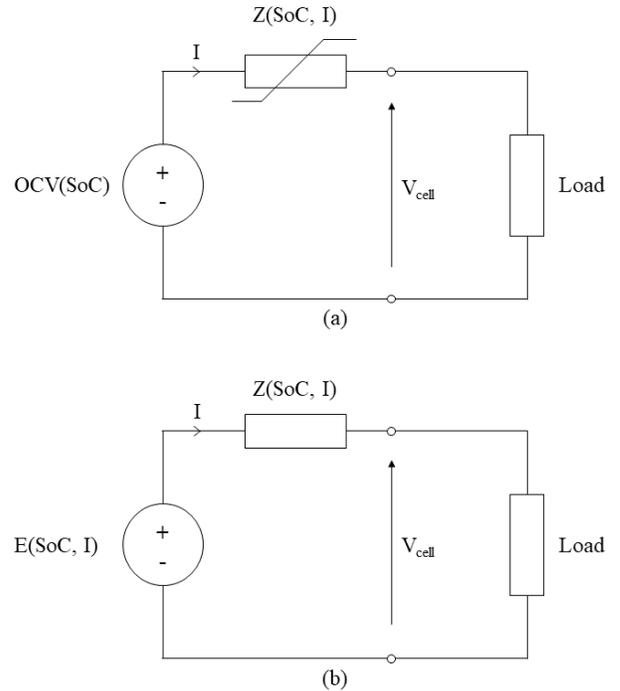

Fig. 2. Illustration of a generic nonlinear equivalent circuit of a battery connected to a load (a) and the corresponding linearized circuit (b).

Impedance is generally defined for a linear circuit element, in AC steady-state conditions, as the complex ratio between voltage and current phasors. The first condition for a valid measurement is therefore that the system is in steady state, which means that any SoC variations and any other transients must be absent or negligible during the whole measurement period. Moreover, since the impedance is defined as a ratio between phasors, it is only valid for linear time-invariant systems, whose response to a sinusoidal perturbation is sinusoidal, at the same frequency. However, a nonlinear system can still be modeled in terms of impedance if it is subjected to small sinusoidal perturbations within a restricted part of its I-V characteristic that can be approximated by a straight line.

To correctly interpret impedance measurements for an electrochemical cell, it is therefore necessary to linearize the circuit of Fig. 2a around a specified operating point, characterized by a well-defined SoC and a DC current (or voltage), under the assumption that the AC perturbation will cause only a small deviation from the specified point. The resulting circuit (shown in Fig. 2b) will have an equivalent EMF and an equivalent series impedance, which both depend on the operating point.

Assuming, without loss of generality, that the impedance is measured by controlling the current and measuring the voltage response (galvanostatic mode), the generic current drawn from the battery, containing both DC and AC (sinusoidal) components, can be written as:

$$i(t) = I_{DC} + I_{AC} \sin(2\pi f t) \qquad (1)$$

where I_{DC} and I_{AC} are the amplitudes of the DC and AC components, respectively, f is the frequency of the AC

component, and its phase is arbitrarily chosen to be zero. The validity of the assumptions required for a meaningful impedance measurement depends on the values of I_{DC}, I_{AC} and f. In more detail, I_{AC} must be small enough to satisfy the linearization assumptions, while the steady-state condition requires the SoC variations caused by the current to be small too.

The shortest possible measurement time is equal to the AC period, i.e. $1/f$. During this time, the DC current will cause a linear SoC variation, whose amplitude is:

$$\Delta\text{SoC}_{DC}(\%) = \frac{I_{DC}}{fQ_{total}} \times 100 \qquad (2)$$

where Q_{total} is the rated cell capacity. On the other hand, the AC current will cause a sinusoidal oscillation in the SoC, whose peak-to-peak amplitude is:

$$\Delta\text{SoC}_{AC}(\%) = \frac{I_{AC}}{\pi fQ_{total}} \times 100 \qquad (3)$$

In both cases, the SoC variation is proportional to the current amplitude and inversely proportional to the frequency of the AC component. It is therefore evident that keeping the SoC variation small is more challenging at lower frequencies; moreover, the nonlinear behavior of the battery is also enhanced at low frequencies. Therefore, satisfying both the steady-state condition and linearization requirements is particularly critical for low-frequency EIS measurements.

The DC current component is not needed for impedance measurements, and indeed laboratory-based EIS is often performed with zero DC current, thus completely eliminating the SoC variation caused by it. However, this solution may be difficult (if not impossible) to achieve in *in situ* EIS, because batteries are normally operated (charged or discharged) with a significant DC current component, and the power converters connected to them may not allow producing a sinusoidal perturbation with bidirectional current flow. Therefore, low-frequency *in situ* EIS requires strategies to effectively deal with large DC components and the associated SoC variation, which will be discussed in Sec. IV.

The AC current component, on the other hand, can and should be as small as possible, not only to keep the SoC variation limited, but also to minimize ohmic losses and to reduce the effect of an oscillating power supplied to the load connected to the battery. The only limitation to lowering the amplitude of the AC component is the decrease of the signal-to-noise ratio (SNR), which may affect the measurement accuracy, so a compromise is required. Again, this may be more challenging to achieve in *in situ* EIS, because the environment is likely to be much more noisy than a laboratory.

According to the equivalent circuit in Fig. 2b, the impedance at frequency f is estimated by measuring the terminal voltage response to the current perturbation in (1) and extracting the AC component from it by removing the DC component, which includes the EMF and the response to I_{DC}. Any SoC variation during the measurement time would have two undesired effects: It would create a transient in the EMF and
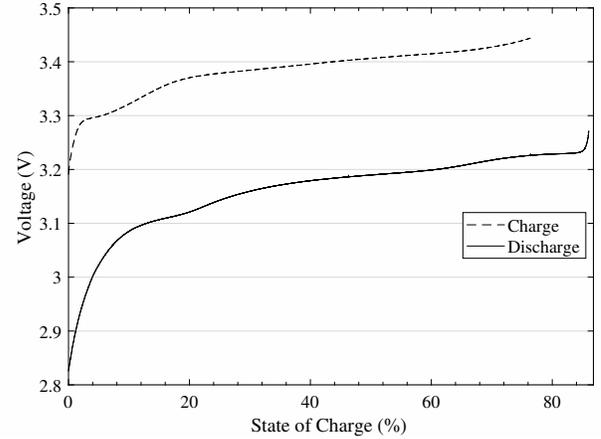

Fig. 3. Charge and discharge curves of the cell under test, measured with a DC current of 0.5 A, corresponding to a C-rate of approximately 0.16.

potentially change the impedance itself, as both the EMF and the impedance are functions of the SoC. Although both effects would violate the requirements for impedance measurements discussed above, a transient in the EMF alone can be effectively managed by suitable signal processing, whereas a significant change in the impedance would completely jeopardize the validity of the impedance measurement. It is therefore important to distinguish between these two effects. A deeper discussion is presented in Sec. IV, with the support of experimental results.

III. EXPERIMENTAL SETUP

The cell used in the experimental investigation reported in this paper is the K226650E02 manufactured by K2 Energy. This is a cylindrical 26650 graphitic carbon/lithium iron phosphate (C/LiFePO$_4$) cell with a nominal capacity of 3.2 Ah at C/5.

Fig. 3 shows the cell's charge and discharge curves measured with a DC current of 0.5 A, equivalent to a C-rate of approximately 0.16. Apart from the extreme parts of the curves, which are highly nonlinear, the two curves can be well approximated by a linear relationship between the SoC and the terminal voltage. Such a linear approximation can be used to estimate the change of the battery EMF caused by a change in the SoC, i.e. $\frac{\Delta V_{OCV}}{\Delta SoC}$, which was found to be 1.7 mV/SoC(%) for the discharge curve and 1.1 mV/SoC(%) for the charge curve.

To perform EIS measurements, the cell is connected to either a programmable DC electronic load (Elektro Automatik EL 9200-70 B) or a fully controllable DC-DC boost converter connected to a resistive load. The electronic load is typically used in laboratory EIS, whereas the DC-DC converter is suitable for *in situ* EIS in commercial applications. Both the electronic load and the DC-DC converter are controlled by an embedded controller (National Instruments CompactRIO 9035) with analog and digital input/output modules. The same system is used to acquire the battery voltage and current
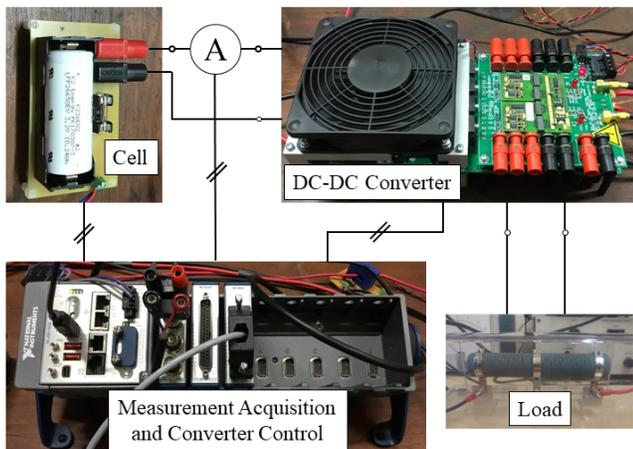
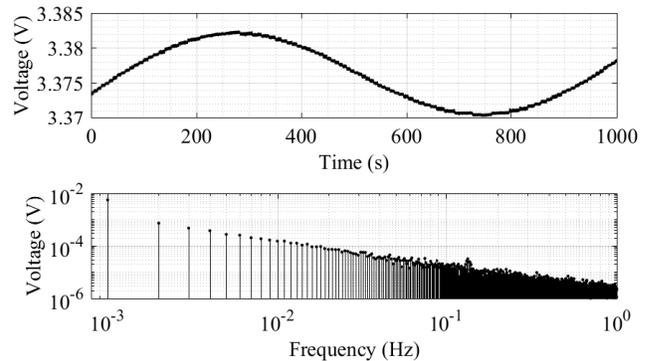Fig. 4. Block diagram of the experimental setup.

Fig. 5. Voltage waveform in the time domain, and corresponding spectrum in the frequency domain, in response to a 1 mHz current perturbation added to a DC current.

signals, with sampling frequencies up to 100 kSa/s. The current measurement is obtained from a Hall-effect closed-loop current transducer (LEM LA 25-P). A block diagram of the whole experimental setup (with the DC-DC converter) is shown in Fig. 4.

IV. Low-Frequency Impedance Measurements

Low-frequency impedance measurements require a long measurement time, during which the state of the battery should remain as steady as possible, as explained in Sec. II. The use of power converters allows performing EIS either during battery discharge or charge. However, in practice, the charge process presents more favorable conditions because the discharge is often characterized by large transients, due to load variations, whereas the battery current is kept constant during most of the charging time and it usually has a lower amplitude than the rated discharge current. Low-frequency impedance measurements are therefore more likely to provide accurate results if performed while charging the battery.

In this section, the measurement of the battery impedance at 1 mHz during the battery charge is reported as an example to discuss the effects of SoC variations on low-frequency measurements and to propose possible methods to assess the validity of the results and to improve their accuracy, when possible. The electronic load is used to control the current supplied by an external DC voltage source to the battery, in order to have a sinusoidal component at 1 mHz, with 50 mA amplitude, superimposed onto a DC current of 0.5 A, according to (1).

Fig. 5 shows the cell's voltage response in the time domain, acquired over one period of the AC component (1000 s), and the corresponding voltage spectrum. A linear, time-invariant system, in steady-state condition, perturbed with a sinusoidal current, would have shown a sinusoidal response at the same frequency, from which the impedance at that frequency could be calculated. The spectrum in Fig. 5, however, clearly shows other significant frequency components, indicating that the conditions above are not satisfied. It is worth noting that

the signal acquisition is synchronized with the waveform generation, therefore there is no spectral leakage caused by a lack of synchronization. Reasons for the observed frequency components can be a nonlinear response of the battery, a transient voltage superimposed to the voltage drop on the equivalent impedance, or a variation of the impedance during the measurement time. As explained in Sec. II, all of these are possible and should be carefully analyzed, as they affect the validity of the impedance estimation in different ways.

From (2), it can be determined that the DC component of the current causes a change in SoC of approximately 4.3% during the measurement time. Using the $\frac{\Delta V_{OCV}}{\Delta SoC}$ estimate obtained from the charge curve in Sec. III, this is found to correspond to a change in V_{OCV} of approximately 5 mV. This is a considerable change which results in a significant rising drift in the measured voltage, as can be seen in the time-domain response of Fig. 5. Such a drift is almost linear (except in the extreme parts of the charge curve), therefore it can be removed from the measured voltage by fitting the curve in the time-domain with a ramp superimposed to a sine wave, using a least-squares method. The resulting signal, shown in Fig. 6, is much closer to a pure sinusoidal waveform. The amplitudes of the second and third harmonics of the 1 mHz have been reduced by approximately 31 dB and 28 dB, respectively, and all harmonics are now below 0.3% of the fundamental component. Assuming the resultant spectral line at 1 mHz in Fig. 6 as the best estimate of the actual value, the uncompensated linear drift would have led to an error of 21% on its amplitude and 0.9 degrees on its phase.

It is important to note that removing the drift caused by the SoC variation leads to valid results only under the assumption that the SoC variation affected only the EMF in the equivalent circuit of Fig. 2b, and not the impedance itself. Any significant impedance change during the measurement time would create a distortion in the voltage response and would not allow using the Fourier Transform to estimate the impedance; in fact, there would be no defined impedance value. It is known that the battery impedance changes as a function of the SoC [3], [4], but the change may be negligible if the SoC variation
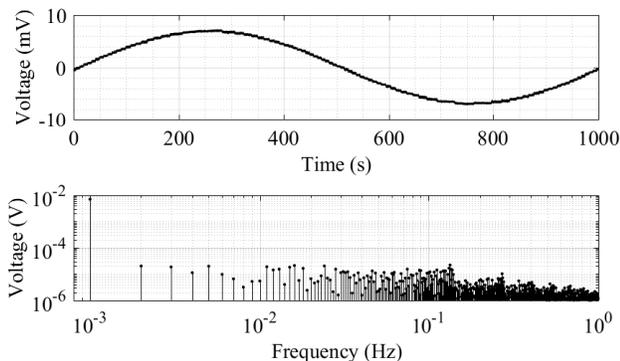
Fig. 6. Voltage waveform in the time domain after removing the linear drift from the waveform in Fig. 5, and corresponding spectrum in the frequency domain.

is limited. This assumption can be justified *a posteriori* by observing the lack of distortion in the voltage signal in Fig. 6. Similarly, the lack of distortion confirms also the validity of the approximation of linear response.

There is, however, another important aspect that must be considered to confirm the validity of the impedance estimate based on the voltage waveform reported in Fig. 6, i.e. the variation of the SoC caused by the AC current component. From (3), it can be seen that a peak-to-peak SoC variation of approximately 0.14% is expected for a 1 mHz current of 50 mA amplitude. This is much smaller than the SoC variation caused by the DC current, but it causes an oscillation in the EMF at the same frequency as the AC perturbation, and it is therefore impossible to distinguish from the voltage drop on the impedance. Such a voltage oscillation may lead to very large errors in the impedance estimate (both magnitude and phase), unless its amplitude is negligible compared to the voltage drop on the impedance, or the oscillation is removed from the signal. It is therefore crucial to estimate the amplitude of the oscillation. A value of 160 μV can be obtained from the $\frac{\Delta V_{OCV}}{\Delta SoC}$ estimate used above, i.e. approximately 1% of the amplitude of the measured voltage waveform. This amplitude is negligible in this case, but a more significant oscillation could have been removed from the measured waveform, based on the estimate of its amplitude and knowing that it has a 90 degree phase difference from the current (as it is in phase with the charge oscillation that is the integral of the current).

V. HIGH-FREQUENCY IMPEDANCE MEASUREMENTS

Battery packs used in most commercial applications are composed of several tens or hundreds of cells, with a combination of series and parallel connections. Accurately monitoring each cell, or at least small groups of cells, requires the acquisition of a large number of voltage and current signals; this is only affordable if multiplexed analog-to-digital converters (ADCs) are used, with several input channels connected to a single ADC. Moreover, the small amplitude of the cell voltage variation requires a good resolution of the ADC to accurately measure the small AC voltage response during EIS. These two

requirements, together, limit the maximum sampling frequency that can be practically achieved in commercial applications, and may therefore hinder high-frequency impedance measurements.

According to the sampling theorem, a signal with frequency components up to f_{max} requires a sampling frequency higher than $2f_{max}$ to preserve the information contained in the original signal; unless this condition is satisfied, aliasing errors occur and the signal can no longer be correctly reconstructed from its samples. However, this requirement can be circumvented if the signal is periodic and stationary, and its frequency content is perfectly known. If the sampling frequency and the observation time are properly chosen, the aliasing errors will change the frequencies of the sinusoidal components of the signal (in a predictable way), but will leave their amplitudes unchanged. As long as different frequency components do not overlap with each other, the aliased spectra of the measured voltage and current waveforms can still be used to correctly estimate the impedance at the various frequency components present in the original signal. It is worth noting that the aliasing will change the phases of both voltage and current signals, but their difference (i.e. the impedance angle) will remain the same.

This technique can be effectively used to achieve high-frequency EIS with a limited sampling frequency, on the proviso that the conditions above are strictly satisfied. In particular, in order to know the spectral content of the signal exactly, the acquisition must be synchronized with the AC waveform generation, and the voltage and current signals must be cleaned from all transients.

The proposed approach is illustrated here with the experimental setup described in Sec. III. The battery is connected to the DC-DC converter and the duty cycle of the converter is modulated with a small multi-sine oscillation, with frequency components at 100.5 Hz and 1000.5 Hz. This modulation creates the same frequency components in the battery current and voltage, as seen in the spectra reported in Fig. 7, obtained by sampling both signals at a frequency of 100 kHz (therefore, with no aliasing), over a time of 10 s.

If the same signals are down-sampled at a much lower frequency of 8 Hz, both frequency components will be affected by aliasing errors, and they will appear at completely different frequencies in the current and voltage spectra, shown in Fig. 8. Nevertheless, the impedance values at the two frequencies, estimated from the aliased spectra, are similar to the (more accurate) values obtained from the original spectra. A comparison is reported in Table I.

Some important points should be noted: The accuracy of the impedance estimation has worsened with the down-sampling because also the noise was affected by aliasing and the SNR has effectively decreased; however, the effect of noise can be reduced by averaging results over time. Moreover, interference from nearby power equipment (e.g. operated at 50 Hz) may be present in the signals, as shown in the current spectra in Figs. 7 and 8, so a suitable sampling frequency should be chosen to avoid any overlapping between the 50 Hz (and its harmonics)
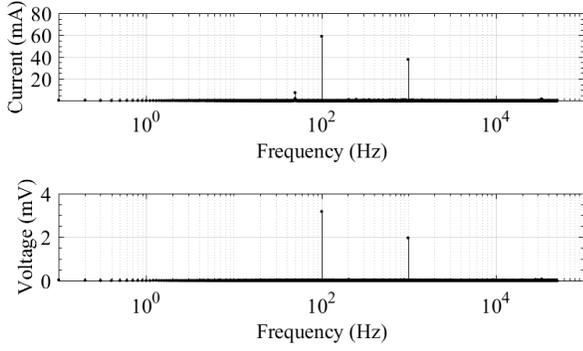
Fig. 7. Frequency spectra of the current and voltage signals sampled at 100 kHz, with no aliasing.
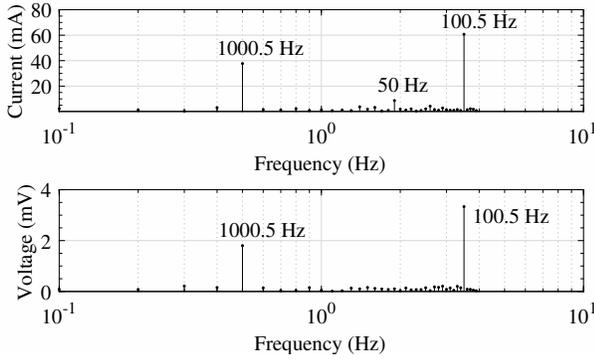

Fig. 8. Frequency spectra of the current and voltage signals sampled at 8 Hz, with aliasing.

and the frequencies of interest in the aliased spectra.

VI. CONCLUSIONS

This paper discussed some important measurement challenges that are likely to affect battery impedance measurements at very low and very high frequencies, particularly when those measurements are performed *in situ*, during battery operation. At low frequencies, the main challenge arises from the validity of the impedance concept, which requires steady-state conditions and a linear time-invariant response of the battery. Strategies to satisfy these conditions and assess their validity were proposed, and limitations were discussed. It was shown that a SoC variation of 4.3% during the measurement

at 1 mHz would have led to an error of 21% in the impedance magnitude, if not compensated for. On the other hand, high-frequency measurements may be hindered by the limited sampling frequency available in commercial applications for the acquisition of voltage and current signals. One possible technique to effectively use a lower sampling frequency to achieve the high-frequency impedance measurements was presented, together with its limitations. Impedance estimates at frequencies two orders of magnitude greater than the sampling frequency were obtained. All proposed methods were validated through an experimental investigation on a C/LiFePO$_4$ cell.

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TABLE I
COMPARISON BETWEEN IMPEDANCE MEASUREMENTS OBTAINED WITH HIGH VS LOW SAMPLING FREQUENCIES.

	f_s = 100 kHz	f_s = 8 Hz
Impedance at 100.5 Hz		
$|Z|$	53.7 mΩ	54.9 mΩ
$\angle Z$	-4.7°	5.7°
Impedance at 1000.5 Hz		
$|Z|$	51.9 mΩ	47.8 mΩ
$\angle Z$	14.6°	8.6°