The Advanced Implantation Detector Array (AIDA)

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Abstract

The Advanced Implantation Detector Array AIDA is a state-of-the-art detector system for the measurement of the decay properties of exotic nuclei at leading fragmentation/fission facilities. Built around highly segmented double-sided silicon strip detectors the positions of both implanted ions and their subsequent decays are measured with high precision which allows for efficient ion- β correlations, even at high implantation rates. A summary of the system and the analysis methodologies used are presented.

Keywords: Fragmentation, DSSD, Implantation

1. Introduction

Decay spectroscopy experiments are a useful tool for probing the properties and structure of radioactive nuclei. The information obtained is relevant to both theories of nuclear structure and also to nuclear astrophysics. In nuclear astrophysics today, many of the questions revolve $_{30}$ around the *r*-process [1] - the astrophysical process responsible for the production of around half the elements heavier than iron. The nuclei of importance to the *r*-process and, therefore, the most desirable to study are very far from stability and often have short decay lifetimes of less than $_{35}$ a second. Obtaining experimental information on these

- a second. Obtaining experimental information on these nuclei requires them to be observed soon after they are produced.
 ¹⁵ In-flight fragmentation or fission of heavier nuclei, such
- as ²³⁸U and ¹²⁴Xe, are common production methods employed that are well suited to the study of short-lived exotic nuclei. Exotic nuclei are produced as the heavy nuclei, accelerated to high-energies of 100s of MeV/u, are impinged on a thin target causing them to undergo fragmentation or fission reactions producing a wide range of exotic nuclei. Fragment spectrometers are then used to filter, focus and identify the produced ions, delivering a

cocktail beam of many isotopes surrounding a central isotope of interest to a detector located at the end of fragment spectrometer within just a few hundreds of ns of the creation of the ion [2]. The detector is then able to measure the subsequent decay of the implanted nuclei. An additional benefit of these methods is the delivery of multiple different species to the experimental area, which can be studied simultaneously. However, random correlations can be a problem in such measurements. Random correlations occur when you ascribe the decay of an implanted ion to the incorrect implanted ion, which results in the formation of background in a measurement. The risk of random correlations being made increases with implantation rate and can be particularly challenging when the mean time between implants is greater than the mean lifetime of the nuclei being implanted.

One method to reduce the number of random correlations made is to use positional information in addition to temporal information when correlating implanted ions with subsequent decays, requiring decays to be observed in the same region of the detector as implanted ions. This technique has been in use since the early 1980s, for example one-dimensional position sensitive detectors were used to correlate the positions of direct proton emission events with implanted ions of ¹⁵¹Lu [3]. By the late 1980s this technique had been further expanded to use a two-

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- ⁵⁰ dimensional position sensitive silicon surface barrier detector allowing the area over which correlations are made to be further restricted [4, 5]. Increased sensitivity with the technique was achieved with the move to double-sided silicon strip detectors (DSSDs), improving on both the po-
- sitional and energy resolution over the previous silicon surface barrier detector [6]. These first DSSDs were optimised for the study of proton and alpha decays from radioactive species produced in fusion evaporation reactions. Their thicknesses ranging from $67 - 110 \ \mu m$ were optimised to sufficiently stop fusion evaporation products, alpha par-
- ticles and protons, while being effectively transparent to β -particles reducing the β -background that would otherwise be observed by the detectors.
- Modern fragmentation facilities require detectors to be ⁶⁵ much thicker than these early detectors as the stopping distribution of different isotopes can cover a range of a few mm in Si. Fortunately, advances in the manufacturing process of DSSDs since these early models have permitted the production of detectors with both larger active areas
- $_{70}$ and increased thicknesses. Larger active areas allow for greater positional separation of the implanted ions, further reducing the number of random correlations, while greater thicknesses make the detectors more suited to the stopping_{105} of high-energy recoil ions produced at current fragmenta-
- ⁷⁵ tion facilities. Examples of systems utilising positional correlations at fragmentation facilities include a β -counting system for fast fragmentation beams at NSCL [7], the Silicon IMplantation Beta Absorber (SIMBA) used at GSI [8] and RIKEN's WAS3ABi (wide-range active silicon-strip₁₁₀
- ⁸⁰ stopper array for beta and ion detection) [9]. A challenge common to these systems is the wide range of energies across implantation and decay events which take place. Stopping ions can deposit more than 10 GeV in a detector while β -particles can deposit less than 1 MeV.
- The Advanced Implantation Detector Array (AIDA) represents the next generation of state-of-the-art silicon detector arrays for performing implant-decay correlation measurements. The system was originally developed for use at FAIR, with one of the principal motivations be-120
 ing the study of β-decay properties of relevance to the
- astrophysical *r*-process[10]. Coupling large area, high segmentation silicon detectors with state-of-the-art fast electronics, AIDA can positively correlate implanted ions and subsequent decays at high implantation rates. At present₁₂₅
- ⁹⁵ two AIDA systems are currently in use. The first is currently located at the Radioactive Ion Beam Factory [11] at RIKEN, Japan and has been in use since 2016. The second is located at GSI and has been used as part of FAIR Phase 0 since 2020 within the DESPEC collaboration [12].¹³⁰
- At both facilities AIDA is used alongside other detector arrays such as BRIKEN [13], DTAS [14], FATIMA [15] or DEGAS [16, 17] providing additional, complimentary information of decay modes involving gamma-rays and neutrons.

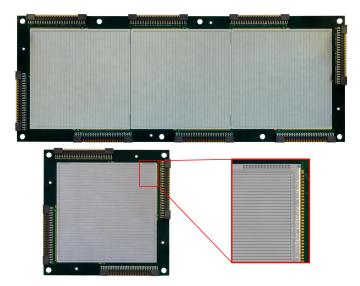


Figure 1: Top image: Photograph of the 'triple' DSSD consisting of three $8 \times 8 \text{ cm}^2$ wafers bonded together. Lower left image: Photograph of the 'single' DSSD consisting of a single $8 \times 8 \text{ cm}^2$ wafer. Lower right image: Zoomed in section of the single wafer, highlighting the individual strips and their bond wires.

2. AIDA hardware

2.1. Double-sided Silicon Strip Detectors

As mentioned in the introduction the use of position sensitive detectors is hugely beneficial in the reduction of random correlations when performing implant decay measurements. Position sensitive detectors are available in many forms each with their own sets of benefits and drawbacks. In the case of AIDA the decision was made to go with segmented silicon detectors, enabling a high degree of positional resolution while still providing the energy resolution required for spectroscopic quality measurements. It is a stack of these highly segmented Double-sided Silicon Strip Detectors (DSSDs) that characterise AIDA. The AIDA system makes use of two differently sized DSSDs depending on the physics aims of the experiment. Common to both sizes of DSSD is the nominal thickness of 1000 μ m, strip pitch of 560 μ m, and inter-strip separation of 50 μ m. The thickness of 1000 μ m is ideal for use at fragmentation facilities where the energy spread of ions results in a stopping distribution that spans up to a few mm in Si. Here with a stack of just a few detectors many ions can be effectively stopped with a single degrader setting while still allowing different atomic species to be spread throughout the stack adding an additional degree of position separation. A thickness of 1 mm also increases the detection efficiency of β -electrons when compared to earlier thin detectors thanks to the increased energy deposition before escaping the detector. The narrow strip pitch of 560 μ m provides a high degree of segmentation, allowing the positions of events to be determined with high accuracy across the surface of the detector.

The smaller of the two DSSD sizes available consists a single Si wafer with an active area of 7.63 cm \times 7.63 cm,

segmented into 128 strips on each side arranged orthogonally resulting in 16,384 voxels per DSSD. A photograph

- of this DSSD is shown in the lower panel of Figure 1. The relatively small transverse dimensions make the detector well suited to being paired with additional detector systems that require high volumetric efficiency around the point of implantation. Examples of these systems include
- ¹⁴⁵ DTAS [14] and BELEN/BRIKEN [18, 13], all of which benefit from being able to maximise the volume surrounding the DSSDs that is covered by a detector in order to maximise their detection efficiencies.
- The second size of detector has been designed to exploit the spatial separation of isotopes in the x direction achievable with the future Super-FRS project [19] at FAIR [20] and with the current FRagment Separator (FRS) [21] at GSI during FAIR Phase-0. With an active area of $\approx 24 \text{ cm} \times 8 \text{ cm}$ and segmented into $384 \times 128 \text{ strips}$,
- ¹⁵⁵ different isotopic species are implanted into distinct regions across the detector minimising random correlations between species. A detector of this size is larger than what can be reasonably achieved from a single Si wafer, the detectors are instead formed by bonding together the n^+n ¹⁶⁰ strips of three of the 8×8 cm² wafers to form one large detector. The benefit of this approach over the use of three smaller detectors is the reduction in electronics channels required to readout signals. For the bonded triple detector a total of 1024 channels are required compared to the 1536
- that would be required to run three individual smaller detectors. A secondary benefit is the minimised loss in dead area between active areas, when comparing the gap between wafers on the wide detector (Upper panel of Figure 1) it can be seen that the distance between individual
- ¹⁷⁰ wafers is much smaller than if three overlapping detectors were used. As different isotopes are now spread across the surface of the detector there is less need to spread them across multiple detectors in the stack. The use of wedge shaped degraders then allows a single detector to be used ¹⁷⁵ to stop all the isotopes of interest providing a greater stop-
- ping power for lighter elements than heavier elements.

2.2. Mechanical assembly

As mentioned in the introduction, one of the primary uses of active stoppers is in decay spectroscopy measurements where one of the decay products is a charged particle. Examples of this can be direct proton emission and β -decay studies. Often these decays leave the daughter₂₀₀

nucleus in an excited state and it is desirable to observe

- the de-excitation of the nucleus whether that be through γ -ray emission or particle emission. For the cases where the de-excitation does not proceed through charged particle emission and instead proceeds through a mode which₂₀₅ the DSSDs are not sensitive to it is desirable to have other detector types sensitive to the emission mode as close to the site of the decay as possible to maximise the detection
- efficiency. AIDA achieves this by mounting the detectors at the end of thin stainless steel rods within a thin, lighttight Al enclosure which is known as the "snout". The

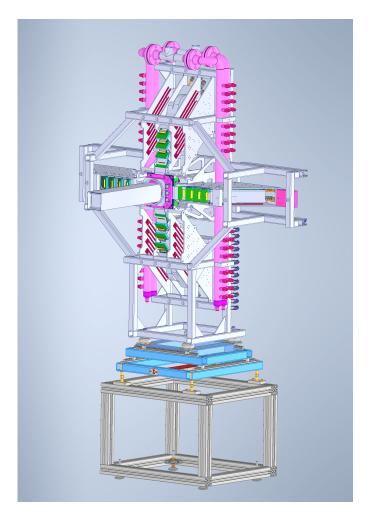


Figure 2: CAD model of the AIDA version currently installed at GSI. The "snout" shown is of dimensions to house the narrow $8 \times 8 \text{ cm}^2$ DSSDs. Green boards arranged at 0°, 90°, 180° and 270° around the "snout" are the adaptor boards that connect the DSSD cabling (not shown) to the FEEs. Pink pipes are water cooling manifolds which distribute cooling water to each of the FEEs. The structure is designed to be easily expanded in the horizontal direction to support the 24 cm \times 8 cm DSSD.

cables for the detectors run alongside the inner surface of the "snout" enclosure, keeping the central region free for the passage of ions. The cables then exit the enclosure at the base of the "snout" where they are connected to the readout electronics. The length of the "snout" enclosure is chosen such that the DSSD and thus the decay site can be located at the centre of the additional detector systems, typical distances from the DSSD to readout electronics are in the range of $\sim 60 - 100$ cm. To minimise the scattering and attenuation of radiation the material budget surrounding the detectors is kept to a minimum, an example of this is that Al snout enclosure is just 0.5 mm thick. The DSSD operate in air.

2.3. Read out electronics

To minimise the amount of noise introduced to the signal between the DSSD and the electronics it is desirable to

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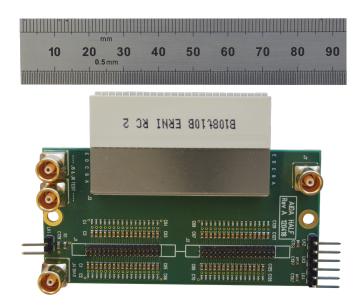


Figure 3: An example adaptor board used for connecting the FEE module to the detector cabling. LEMO connections provide connections for a test input (top left), bias line (bottom left) and the ground plane (top right). Jumper pins allow optional grounding of various parts of the detector package. High-density IDC connectors (middle) are connected to the DSSD cabling.

have the electronics located as close the DSSD as possible. 210 With detector stacks that require over 1000 channels of instrumentation, providing instrumentation for all of these channels close to the DSSD is a challenge requiring the use of high-density electronics. In the following sections the components in the chain from DSSD to data-acquisition $^{\rm 240}$ 215 are detailed.

2.3.1. Detector cabling and adaptor boards

Electrical connections to the DSSD are made over low profile 68 pin Samtec FTSH headers [22]. Each connector²⁴⁵ provides readout for 64 strips on the DSSD and provides 220 additional connections for PCB ground, detector bias and field plates. Short ($\sim 105 \text{ mm}$) kapton couplers provide a low profile, minimal material, intermediate connection from the DSSD to bulkier Samtec ribbon cables [23] further²⁵⁰ along the snout where there is less requirement to minimise 225 the material budget. The use of screened kapton cables for

the full length between DSSD and electronics was found to result in large capacitative load for the preamplifers and unacceptable noise performance.

230 connect to PCB (printed circuit board) adaptor cards, illustrated in Figure 3, which act as the interface between the detector cabling and the readout electronics. Traces on the PCB map the signals from the DSSD to an ERNI input connector [24] to the front end electronics. LEMO²⁶⁰ 235 connectors on the PCB provide detector bias and test in-

- The ribbon cables exit at the base of the snout and $^{\rm 255}$
- puts. Typical values for the test and ac coupling capacitors on the board are 0.7 pF (same as LEC C_f providing unity gain test input) and 33 nF/100 V/X7R respectively. The

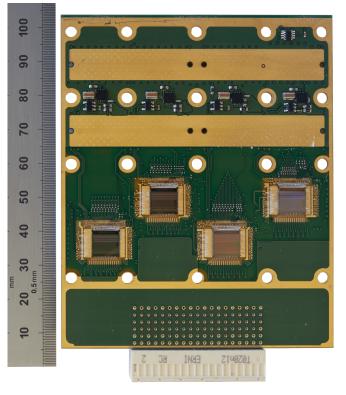


Figure 4: Photograph of the FEE mezzanine board. The four ASIC chips that are wire bonded to the board are visible in the lower half. 95-pin ERNI connector [24] shown at the bottom. Connectors to the FEE64 PCB not shown (reverse side of ASIC PCB).

upper right connector (Figure 3) is a high-quality connection to the PCB ground. Jumper pins seen on the left and right of the adaptor board in Figure 3 allow parts of the DSSD such as the DSSD PCB, and the front and back field plates to be grounded to the adaptor card PCB ground which in turn can be connected to a mechanical ground via the LEMO connector.

The use of a simple adaptor PCB between the DSSD and front end electronics means that it is straightforward to use other silicon strip detector designs as required. For example, AIDA hardware has been used with MSL type W1 ($16 \times 16, 50 \times 50 \text{ mm}^2$), BB7($32 \times 32, 64 \times 64 \text{ mm}^2$) and TTT14 $(128 \times 128, 98 \times 98 \text{ mm}^2)$ [25] detectors.

2.3.2. Application Specific Integrated Circuits (ASICs)

One of the design goals of the AIDA electronics was to provide good energy resolution for both the implanted ions and also their subsequent decays. In the case of β decay experiments β -particles will deposit energies ranging from 10s of keV up to a few MeV in the DSSD whereas stopping implanted ions can deposit up to 17 GeV. This defines an energy scale where the minimum energy we hope to measure in our system is one-millionth of the maximum possible energy deposition. To provide good energy resolution across this broad range of energies poses a significant challenge in the handling of signals. To meet these requirements, a 16-channel Application Specific Integrated Circuit (ASIC) was designed to handle the readout of the signals. A detailed description of the design of the ASIC can be found in Ref. [26], however, a short description of the features that provide the required functionality is presented here.

One of the defining features of the AIDA ASIC is found in it's analogue front end, where each strip is connected to two preamplifier - and subsequent signal processing channels. The first is the high energy channel (HEC) with a low gain preamplifier for measuring high energy im-275 plants. The second channel is connected to a preamplifier with selectable gain - a low energy channel (LEC) with high gain for measuring low energy radioactive decays, and a medium energy channel (MEC) with intermediate gain for measuring higher energy ions. AIDA is therefore 280 able to provide high resolution measurements across a wide range of energies. The selectable gain channel allows the Full Scale Range (FSR) to be set to either 20 MeV or 1 GeV, whereas the low-gain preamplifier has a FSR of $\approx 20~{\rm GeV}$ (Maximum energy deposition of $^{238}{\rm U}$ in 1 mm

 $\approx 20 \text{ GeV}$ (Maximum energy deposition of 4 Si $\approx 16.6 \text{ GeV}$).

The preamplifiers are configured such that only the selectable-gain preamplifier is directly connected to the input channel, while the low-gain preamplifier is connected via a bypass link. When a small energy signal is input, such as a decay event, only the selectable-gain amplifier is connected to the input and the signal processing is handled entirely within this circuit. However, when a high-energy implant event occurs the charge deposited in the detector

- saturates the selectable-gain channel and a CMOS bypass switch opens, providing a path to the low-gain preamplifier. This connection is maintained until the integration of the input charge is complete. Once the integration of³²⁵ the input charge is complete and the peak is held in a
- peak hold circuit the low-gain preamplifier is disconnected from the input. A series of pulses are then sent to the high-gain/low-energy channel to remove the charge and return the signal to baseline. This allows the circuit to be³³⁰ ready to measure new signals in as little as a few tens of microseconds following the large implant signal, removing
- the need to wait many time constants of an RC circuit in conventional electronics.

The availability of the dedicated low-gain/high-energy³³⁵ channels is one of the features that sets AIDA apart from

³¹⁰ previous iterations of active stoppers. Often in these previous generation of detectors, logarithmic preamplifiers were used to handle the high-energy signals, effectively losing any energy information about the incoming implant. With³⁴⁰ AIDA this energy information is not lost and we have

- ³¹⁵ demonstrated an energy resolution of ~ 20 MeV or ~ 0.1% FSR. The typical dynamic range of an ASIC is ~ 1 : 2000. The additional information provided by the energy of the stopping ions can be used as an additional measure of the Z of the implanted nuclei and also the discrimina-
- tion of charge states, a common contaminant in the particle identification spectra produced by fragment separators.

A summary of the performance and specifications of the AIDA ASIC can be found in table 1.

No. channels	16
Input polarity	positive & negative
Gain	HEC and LEC or MEC
Low energy channel (LEC)	20 MeV FSR
Medium energy channel (MEC)	1 GeV FSR
High energy channel (HEC)	20 GeV FSR
Noise $e_0 \ (\tau = 8\mu s)$	
LEC	< 10 keV rms
MEC	< 500 keV rms
HEC	$< 20 {\rm ~MeV} {\rm ~rms}$
Noise slope $e_{\rm ns}$ (LEC, $\tau = 8\mu s$)	$\mathbf{TBC} \ \mathrm{eV/pF} \ \mathrm{rms}$
Integral non-linearity	0.1% > 90% FSR
Shaping time (4 bit register)	$0.5 - 8 \ \mu s, 0.5 \ \mu s$ increment
Slow comparator (8 bit register)	
LEC	10 keV/LSB
MEC	500 keV/LSB
Fast comparator (8 bit register)	
LEC	100 keV/LSB
MEC	5 MeV/LSB
HEC	100 MeV/LSB
Multiplex readout	32:1
ASIC Clock	500 kHz
Power	TBC
Die Size	$7.9 \times 7.5 \text{ mm}^2$
Process	AMS 0.35 μm CMOS

Table 1: AIDA ASIC specifications

2.3.3. Front End Electronics

The electronics that handle the data output of the AIDA ASIC are mounted on a module that is known as the Front End Electronics (FEE64) module. Each FEE64 module is responsible for handling the signals from 64 channels of instrumentation, the number of strips handled by one connector on the DSSD. The FEE64 modules contain all of the electronics used in the processing of the signals and act as their own independent data acquisition systems. The requirements for these modules was that they need to be both relatively compact, to allow them to be located close to the DSSD, and also that they need to be able to handle a high throughput of data, to support the high beam intensities available at modern fragmentation facilities.

At the front of the FEE module are four ASICs wire bonded onto a mezzanine board which provides the signal processing for the 64 channels of instrumentation the FEE is responsible for. In operation these mezzanine boards are covered with a copper plate which acts as an EMI/RFI shield and also a cooling plate to dissipate excess heat while providing a light tight enclosure for the ASICs. Figure 4 shows one of these mezzanine boards without the copper plate, the four ASIC chips are arranged on the lower half of the board. Each ASIC is connected via a 32:1 multiplexer to an Analog Devices 7980 16-bit, 1 MSPS
ADC [27] with an input range of ±2 V. ASIC channels with valid data are sequentially readout via the multiplexer controlled by a 500 kHz clock. As the FEEs process signals of both polarities zero output nominally corresponds to ADC channel 32768 with FSR positive and negative signals corresponding to ADC channels 0 and 65536 respectively. This works out to be about 0.7 keV/ch for

the LEC and 0.7 MeV/ch for the HEC. The ADC readout, data processing, building of events,

and control of the FEE64 is handled by a Xilinx Virtex 5 [28] Field Programmable Gate Array (FPGA) which includes an embedded PowerPC processor. The data acquisition program runs on the Linux operating system installed on the PowerPC processor. Each FEE64 is therefore a fully integrated and independent data acquisition

- 365 system. ADC data is stored using the Total Data Readout (TDR) GREAT data format [29], with each data item stored as a pair of 32bit words. Contained in these words is the ADC value itself, a partial timestamp relative to the FEEs internal 100 MHz clock, a flag indicating whether it
- was from the high or low gain amplifier, and information on the channel and FEE in which the event was recorded. In addition to the ADC data items, information data items are included in the data stream which provide information on the running of the FEE and also contain the re-
- ³⁷⁵ quired information to reconstruct the full timestamp of ADC events.

The AIDA ASIC preamplifier outputs are also connected (via signal conditioning op-amps) to eight Analog Devices 9252 Octal, 14-bit, 50 MSPS ADCs [30]. With these, the waveforms of signals in each of the low-energy preamplifiers can be observed, a useful tool in debugging and investigating the noise performance of the electronics. There is also the ability to record or process the waveforms of events to file. While this feature has not yet been utilised in operation it would allow additional offline analysis to

be performed on the waveforms such as pulse shape discrimination.

Synchronisation is maintained between each of the FEE cards via the use of an external 50 MHz clock, which is provided to each of the FEE cards over a High Definition Multimedia Interface (HDMI) connection. To date two different timestamping and synchronisation systems have been used. For experiments at RIKEN using AIDA as part of BRIKEN, the timestamp was a 48 bit scaler which incremented in 10 ns intervals and synchronisation was maintained between systems with a correlation scaler as detailed in Ref [18]. In experiments at GSI the timestamp was stored as a 64-bit number representing the time in ns. Here the timestamp was externally provided by the WhiteRabbit system [31] which provides a common timestamp

to the entire facility allowing all systems to be aligned in time to high precision. As both of these timestamp values would take up too much of the available 64-bits of space⁴³⁵ in the GREAT data format event words the timestamp is split into multiple parts. The lowest 28 bits of the times-

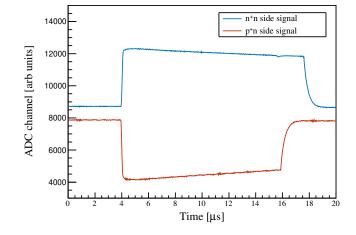


Figure 5: Example waveform output from the 50×10^6 samples per second ADC located on the FEE card. Signals were triggered by a common positive polarity pulser with polarity for n^+n strips inverted with a Cooknell SA1 Sum & Invert Amplifier.

tamp is attached to every event whereas the upper bits of the timestamp are instead added to the datastream at fixed intervals as SYNC statements with a rate greater than the rollover period of the bits.

3. AIDA in operation

3.1. DAQ Control

With each single DSSD requiring 4 FEE cards and each triple DSSD requiring 8 FEE cards it is not unimaginable that an experiment could require upwards of 24 FEE cards in total. Controlling each of the FEEs individually would not be feasible, instead, commands are issued from a central server which then communicates with each of the embedded CPUs on the FEE cards. From the server the user is able to reset, setup, start and stop the data acquisition program running on each embedded processor. The user can also configure, control and monitor all aspects of ASIC and FEE64 operations, for example, specifying the ASIC input signal polarity, shaping time, and comparator thresholds.

The data acquisition program running on the FEE64 embedded processor creates a number of spectra which are stored in memory on the FEE card. The spectra show LEC (or MEC) and HEC ADC data, ADC and fast comparator hit patterns, and preamplifier waveforms. An example of preamplifier output signal waveforms is shown in Figure 5 where it can be seen that the high sampling rate allows the visualisation of the waveform in the low energy channel.

3.2. Merger and TapeServer

The data from each of the FEEs connected to the central server is read out and merged by a dedicated Merger program [32]. he program forms TCP (Transmission Control Protocol) links with each of the FEEs to efficiently

read data from their memory buffers as 64 kB blocks. The data from the buffers are then merged into a single time ordered list using the SYNC information items that are embedded in each of the data streams. A second program

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known as the TapeServer [32] forms an additional TCP link with the output of the merger program. It is the Tape-Server that is responsible for writing the data to disk on the AIDA server. Data file names are automatically incre-445 mented when the size of the file reaches a pre-determined

size (usually configured as 2 GB). In many cases it is desirable to also forward the data onto other systems while also writing the data to file. Examples of this include running an online data analysis pro-450 gram to monitor the quality of the incoming data and also in forwarding data to other experimental systems to monitor synchronisation between systems. This is achieved

by placing a "data spy" on the link between the Merger and the TapeServer, allowing the extraction of data blocks 455 from a shared memory segment in the TapeServer. For example, in recent experiments at GSI this methodology has been used to forward AIDA data onto the MBS DAQ system used by the DESPEC collaboration [33].

3.3. Data sorting and event building 460

The data recorded by the DAQ consists of a time ordered data stream of of individually timestamped ADC and discriminator data. The actual building of "decay" and "implant" events from this stream requires further 465 analysis which can be done either online during the experiment for a quick analysis or offline for a more complete analysis. The methods used can be split up into four main stages; data unpacking, event building, calibration and clustering.

470 3.3.1. Unpacking

During the unpacking stage data is read into the program in 64 kB data blocks, from which the data words rep-500 resenting individual data items are extracted. Depending on whether the data item is an ADC item or an information item values for FEE#, Channel, Timestamp and ADC 475 data, or FEE#, information type, information value and Timestamp are obtained respectively. As the timestamp included in each event only contains the least significant⁵⁰⁵ bit (LSB) the full timestamp is reconstructed by combining with the most recently updated value of the most signifi-480

cant bit (MSB), which is sent as an information data item. During the unpacker stage the extracted ADC data items are placed into a time ordered list which is then $passed^{510}$ onto the event builder stage of the program.

3.3.2. Event building 485

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As each channel in AIDA is self triggered, there is not 515a conventional "trigger" and "event window" which defines a period in which all ADC events are assumed to be correlated. Instead the correlation of data items must be done during the sorting. The logic with which this is done

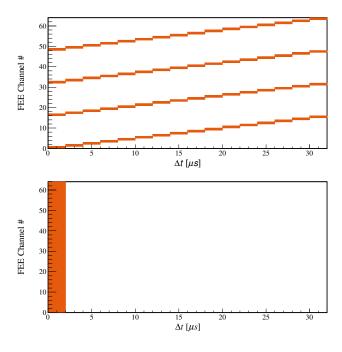


Figure 6: Time distribution of pulser events within a FEE card, relative to the first timestamp recorded for that event. Top plot shows the uncorrected distribution, clearly demonstrating the effect the multiplexer has. The bottom plot shows the distribution once corrected, with the majority of events now observed to occur at the same time.

is dictated by the multiplexed output used in the ASICs and specifically the time period between successive readouts. For example, as the time period between successive readouts in the ASIC is 2 μ s, if two adjacent channels in the ASIC simultaneously record an event the first read out will be given a timestamp $t = t_0$ while the second is given a timestamp of $t = t_0 + 2 \mu s$. As a result we say that there is a chance that all events with a time difference Δt of $\Delta t < 2 \ \mu s$ may be correlated and define gaps between successive events of greater than 2 μ s as an event boundary.

With the event boundaries defined it is then necessary to correct the timestamps of items within the window for the delay in read out resulting from the multiplexer. The need for this correction can be clearly seen for events where a pulser signal is input to all FEE64 channels simultaneously, as shown in Figure 6. The upper panel shows the non-timestamp corrected read out time of data items within a FEE with respect to the first timestamp of that event window. The timestamps of successive ASIC channels are observed to increment in 2 μ s steps with the first channel of each ASIC being aligned. The timestamps for the multiplexed output are easily corrected by keeping track of the number of data items already recorded by an ASIC within the event window. The result of this correction applied to the pulser events is shown in the lower panel of Figure 6, where it is observed that following the correction that all of the pulser items are now observed to occur at the same time.

After the timestamp correction, the data items within an event window are put into one of two lists depending on whether the data item corresponds to LEC or HEC data. If the event window contains two or more data items that originate from the high energy channel (the minimum number of data items required to determine a position on a DSSD), the event window is defined as an 'implant event'

- and only the high energy data items are passed on for calibration. The reason only the high energy data items are passed on is due to the large amount of charge deposited ⁵³⁰ in the detector, strips adjacent to the implant location will also observe an amount of charge which could be mistaken as a decay if not rejected. If there are no high-energy data items but the number of low-energy data items is
- greater than half the total number of strips in the system the event is defined as a 'pulser event' and these data items are filtered out. Finally, if neither of these conditions are fulfilled and there are two or more low-energy data items, the event is defined as a 'decay event' and the low energy data items are passed on for calibration.

540 3.3.3. Calibration

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In the calibration stage the data items are converted⁵⁸⁰ from FEE# and channel to DSSD, front (p+n junction) or back (n+n Ohmic) side, and strip. Due to complexities in the DSSD readout and the adaptor board layout there is not a 1:1 mapping between channel number and strip

number which must be corrected for during this stage. ⁵⁸ For many experiments involving AIDA an absolute energy calibration is not required and a nominal conversion from ADC channel to energy is performed. The first step

in this process for the low-energy data items is the correction for the ADC offset. This value is found by performing a pulser walkthrough before/after an experiment,⁵⁹⁰ in which pulser signals of known relative amplitudes are fed into the channel. By determining the centroids of the peaks for each channel an ADC offset for each channel can

be obtained. A nominal gain of 0.7 keV per ADC channel is then applied to the offset corrected value which is de-⁵⁹⁵ termined by the nominal overall gain of the preamplifier, shaping amplifier, peak hold, MUX and ADC signal processing chain. The variation from channel to channel is dominated by the preamplifier feedback capacitance ($C_{\rm f}$). By careful ASIC design and layout the variation of $C_{\rm f}^{600}$ from channel to channel is kept to within c. 3%. ADC offset correction therefore produces ADC data which are gain matched to a few % level, which is sufficient for many applications (e.g. low energy beta events).

For applications where higher precision gain matching⁶⁰⁵ is required a method using in beam data has been developed based on the techniques given in Ref. [34]. Tradi-⁵⁷⁰ tional calibration sources are not well suited to the calibration of the system due to the challenges of being able to illuminate all detectors within the stack. Instead, in beam⁶¹⁰ light ions, produced during the fragmentation of heavy ion are used. These ions are well suited to calibration ⁵⁷⁵ as they are not stopped in the silicon and instead punch

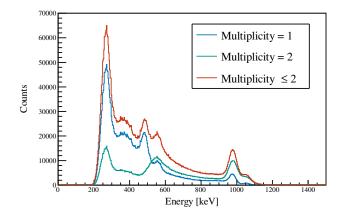


Figure 7: Energy spectrum of a 207 Bi conversion electron source gated on different multiplicities of strips firing.

through the entire detector stack in a straight line, depositing $\approx 1-3$ MeV in individual pixels of each detector. The method presented in Ref. [34] allows these events to be used to provide a relative gain match for all strips on a detector, but not between detectors as the absolute energy deposited by the light ions is not known. An absolute gain match is then found by performing a background run and identifying α -decay events from the naturally occurring Uranium (4n+2) decay series present in the atmosphere.

Once calibrated, the data items are placed into a list sorted by detector, then side, then by strip such that data items from adjacent strips are adjacent events in the list.

3.3.4. Clustering and pairing

 β -electrons can have a range in silicon of greater than the strip pitch of the detector, for example, a 1 MeV electron has a range of 2.3 mm in Si. β -particles may therefore deposit energy across multiple adjacent strips. To account for this the contributions of adjacent strips that produce data within the same event window are summed together to form clusters. The impact of clustering can be seen in Figure 7 which shows the energy spectrum observed for a $^{207}\mathrm{Bi}$ conversion electron source gated on different cluster size multiplicities. When gated on cluster sizes of a single strip (blue line), it can be seen that the majority of electrons observed are low in energy. Some higher energy electrons are observed as it is possible that they propagate through the Si parallel to the direction of the strip. When looking at the energies recorded by clusters of two adjacent strips (green line) the majority of electrons are observed at higher energies, with less observed at lower energies.

The absolute positions of events on the DSSD surface are found by matching clusters from the front side of the detector with the back side of the detector. As both the front and back clusters should record the same energy an equal energy cut is used as a matching condition, reducing the number of random correlations that could otherwise occur. The position of the event is defined to be at the geometric centre of the now 2-dimensional cluster. The 615

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size of the cluster, e.g. the width and height, is also stored which allows the event area to be used in later stages of analysis.

For 'decay' events clustering is performed for all of the⁵⁷⁰ detectors present in an event window with the final paired clusters written to an output file in time order. For implant events it is often only the layer in which the ion stops that is required in the final analysis. As such this stopping layer is calculated and only high-energy events occurring⁶⁷⁵ in this layer for a given event window are written to file. The stopping layer is defined as the layer in which the following conditions are met: that for both the front and back side of the detector there is at least one high-energy data item in each, that there are no high-energy data items⁶⁶⁰ in downstream detectors, that all upstream detectors have

at least one high-energy data item in. With these conditions a stopping layer and position can be assigned to the majority of high energy event-windows.

3.4. Implant decay correlations

- Correlations between implanted ions and their subse-⁶⁸⁵ quent decay products are done based on the positional ⁶³⁵ information available for the events. For decay products such as protons and α particles, which have a relatively small range in Si, it is often enough to just look for cor-⁶⁹⁰ relations in the same pixel in which the ion is implanted. However, for β -decays which have a much greater range
- ⁶⁴⁰ in Si it is necessary to look for correlations across a wider area. Conventional methods of correlating β -decays with ⁶⁹⁵ implants in pixelated detectors define an area of a few mm² around the implant location and correlate with all decay events that fall within this area. While this method
- ⁶⁴⁵ provides a reasonable correlation efficiency it comes at the cost of an increased random contribution. This increased random rate can be down to the narrower strip pitch of the AIDA detectors compared to previous implantation detectors, requiring a greater number of strips to be included⁷⁰⁵
- ⁶⁵⁰ in the correlation window. To reduce the random correlation rate a new method was developed that makes use of the full positional information for the events obtained, with AIDA and does not just use the central location for implant and decay events. Here correlations are defined
- ⁶⁵⁵ between implants and decays when their respective cluster areas on the DSSD surface are either overlapping or adja-₇₁₅ cent to one another. With this method it was found that the same β -decay detection efficiency could be obtained as with conventional methods, but with a significantly reduced random contribution rate. Detailed discussions of₇₂₀
- the analysis of implant-decay correlations is provided elsewhere [18, 35]. For detailed discussion of data fitting and the evaluation of random and systematic errors see [36].

4. Summary and Outlook

In this paper the base characteristics of the AIDA detector system have been presented. AIDA has now resulted⁷³⁰ in two PhDs [36, 37] in which further information can be found. The two systems have been used in successful experimental campaigns at RIBF RIKEN [38, 39, 40, 41] and also at GSI as part of FAIR Phase 0 [42, 43, 44]. Across these campaigns the performance of the systems have been demonstrated with the use of ion gated implant-decay correlations resulting in the measurements of new P_{xn} values and excited state lifetimes. At other laboratories similar systems are currently in development [45]. In the near future it is envisioned that AIDA will continue to be used as the core component of DESPEC with the other setup ready to be run in parallel at RIKEN. During this time the operation of the system will continue to be refined. With the eventual start of "Day 1" science at FAIR, AIDA will be well placed to perform cutting edge science exploiting the radioactive ion beams available via the Super-FRS.

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